

IN THE CLAIMS

Please amend the claims as follows:

1. (original) A method of testing analogue or radio frequency circuitry for the presence of faults, the method comprising the steps of:
  - a) applying a plurality (100) of different DC power supply voltages to a circuit component under test, at least one of said power supply voltages being arranged to cause at least some of the elements of the circuit or component under test to operate in a predetermined region of operation; and
  - b) measuring (102) the quiescent current of said circuit or component as a result of application of said power supply voltages to generate (104) a current signature representative of the operation of said circuit or component;  
the method being characterized in that said power supply voltages at which said quiescent current measurements are taken comprise selected distinct voltages; and by the step of:
  - c) comparing (106) said generated current signature with a predetermined current signature representative of operation of a fault-free component or circuit so as to determine (108) whether or not any faults are present in the component or circuit under test.

2. (original) A method according to claim 1, including the further step of measuring one or more selected nodal voltages, in addition to said quiescent current, as a result of application of said selected power supply voltages.
3. (original) A method according to claim 2, wherein said one or more nodal voltages are measured at one or more respective output nodes of said circuitry.
4. (currently amended) A method according to ~~any one of claims 1 to 3~~ claim 1, including the step of providing a single supply voltage means and ramping said supply voltage up to attain each of said selected power supply voltages, prior to measurement of the quiescent current.
5. (currently amended) A method according to ~~any one of claims 1 to 4~~ claim 1, wherein the selected power supply voltages are selected so as to cause at least some of the elements of the circuitry under test to pass through several regions of operation.
6. (currently amended) A method according to ~~any one of claims 1 to 5~~ claim 1, wherein a fault dictionary database is provided, and the method includes the further step of comparing a generated

current signature with contents of such a database to diagnose one or more faults present in the circuitry under test.

7. (currently amended) A method according to ~~any one of claims 1 to 6~~ claim 1, wherein a tolerance window is defined for the resultant quiescent current measurements for at least one of the selected power supply voltages.

8. (original) A method according to claim 7, wherein a tolerance window is defined for the resultant quiescent current measurements for all of the selected power supply voltages.

9. (currently amended) A record carrier on which is stored a computer program for enabling the method of ~~any one of claims 1 to 8~~ claim 1 to be performed.

10. (currently amended) A method of testing analogue or radio frequency circuitry, including the step of making available for downloading a computer program for enabling the method of ~~any one of claims 1 to 8~~ claim 1 to be performed.

11. (original) Apparatus for testing analogue or radio frequency circuitry for the presence of faults, the apparatus comprising

a) means for applying a plurality of different DC power supply voltages to a circuit or component under test, at least one of said power supply voltages being arranged to cause at least some of the elements of the circuit or component under test to operate in a predetermined region of operation; and

b) means for measuring the quiescent current of said circuit or component as a result of application of said power supply voltages to generate a current signature representative of the operation of said circuit or component;

the apparatus being characterized in that said power supply voltages comprise selected distinct voltages; and by:

c) means for comparing said generated current signature with a predetermined current signature representative of operation of a fault-free component or circuit so as to determine whether or not any faults are present in the component or circuit under test.